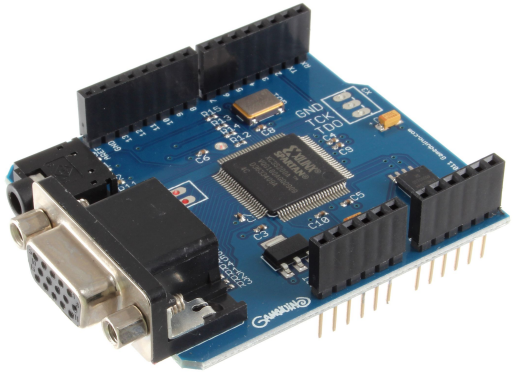
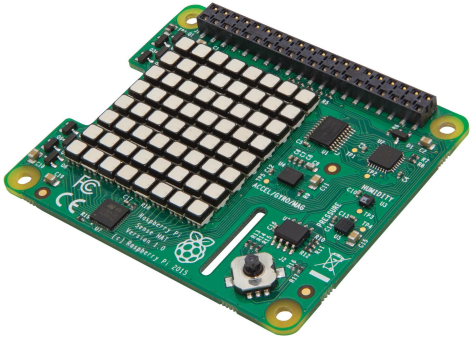
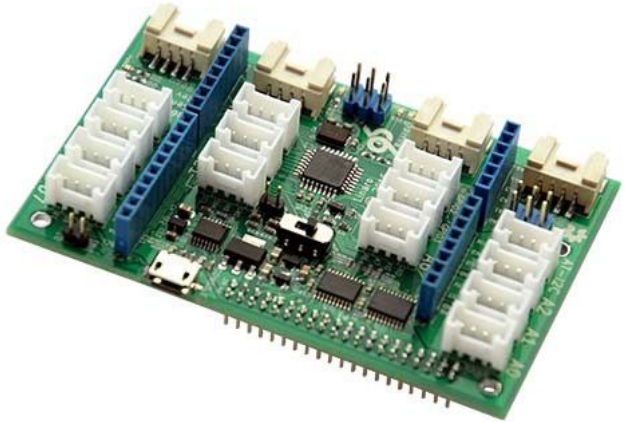
Current Status of the 96Boards Mezzanine Ecosystem

Thore Krug
Open Hardware Enthusiast

Introduction to the 96Boards Mezzanine







Arduino Shield

5V

Raspberry Pi Hat

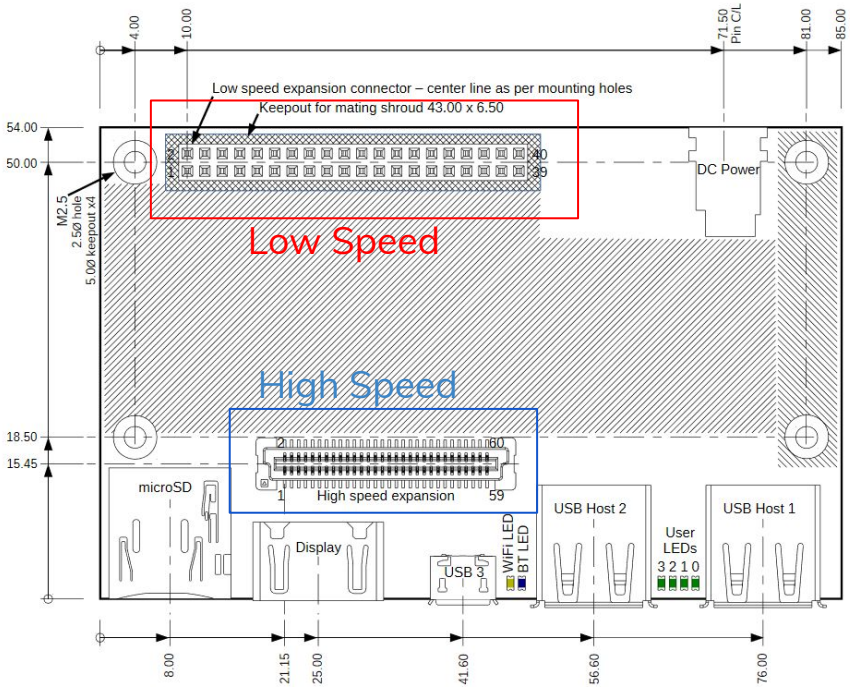
3,3V

96Boards Mezzanine

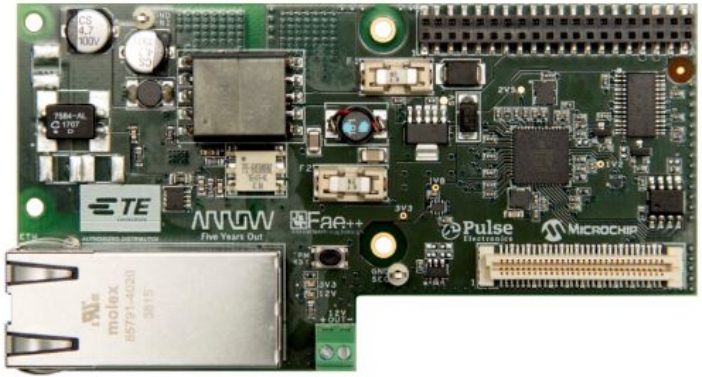
1,8V

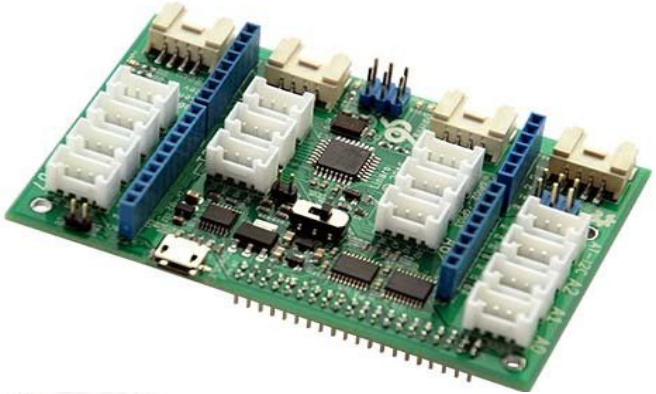
Available Interfaces

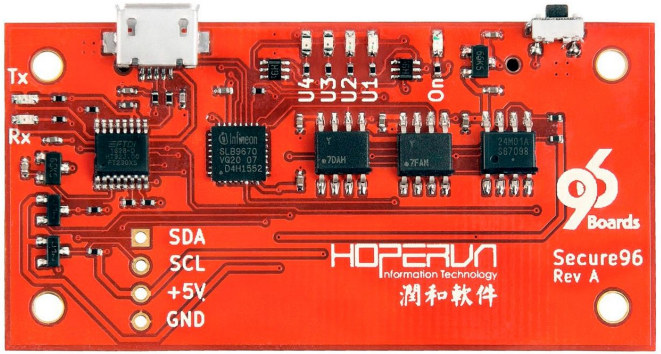
- High Speed:
  - USB
  - MIPI DSI
  - SD or SPI
  - MIPI CSI-2
- Low Speed:
  - SPI
  - I2C x2
  - I2S
  - GPIO x12
  - Reset & Power



96Boards Mezzanine Form Factors



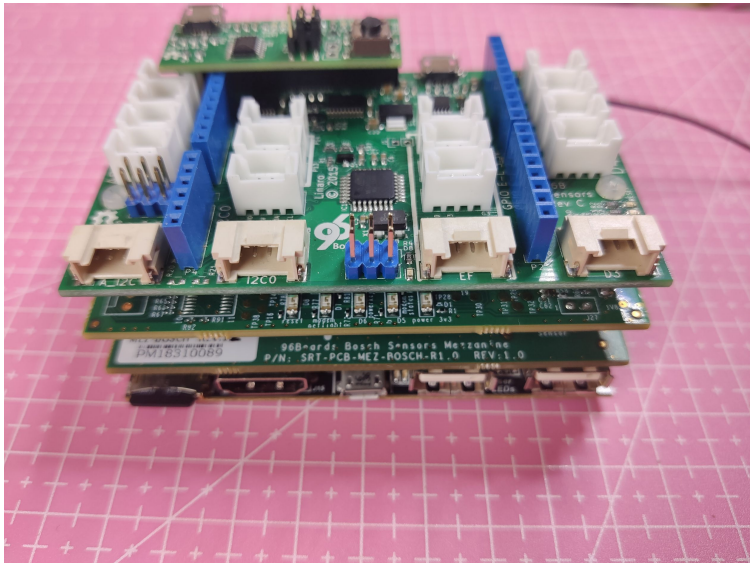




Mezzanine Header Support Matrix

Product	Low Speed Header	High Speed Header
Consumer Edition		
Consumer Edition Extended		
IoT Edition		
IoT Edition Extended		
Enterprise Edition		
SOM Edition (Carrier)		

Stackability

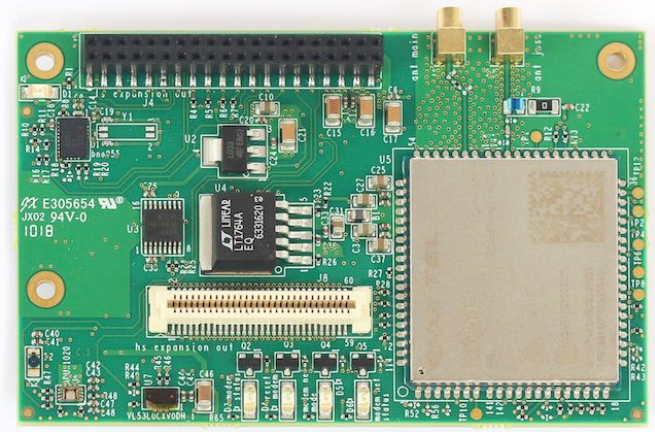


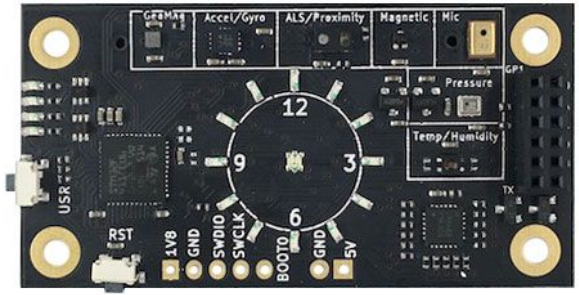
96Boards CE Edition

96Boards CE Extended Edition

Current Mezzanines

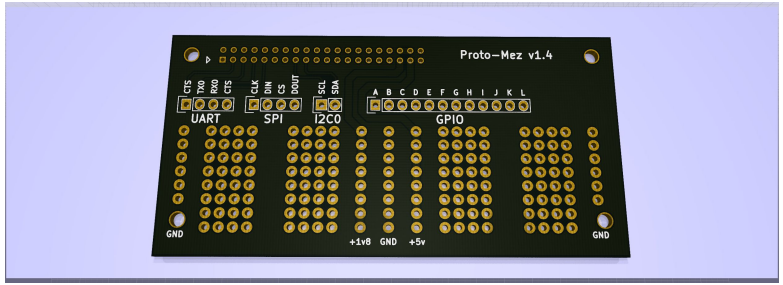
- Camera Mezzanines
- Security Mezzanines
- Connectivity Mezzanines
- Mezzanines for Maker
- FPGA Mezzanines



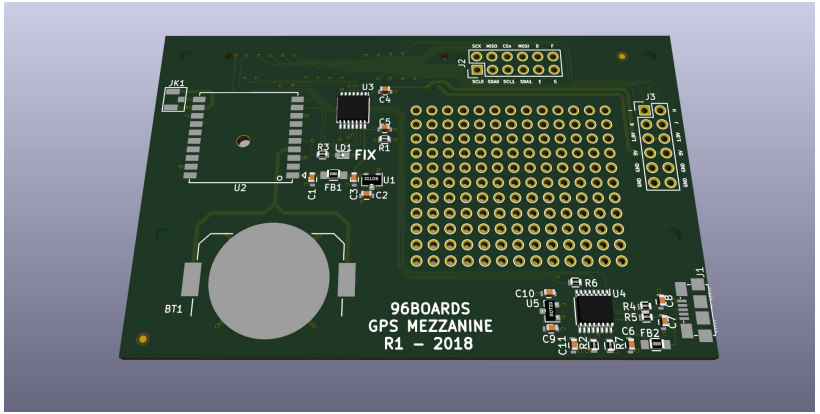


https://96boards.org/products/mezzanine/

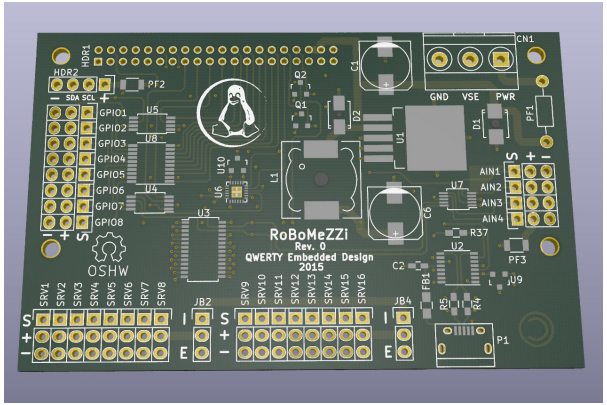
Community Member Mezzanines



Proto Mezzanine

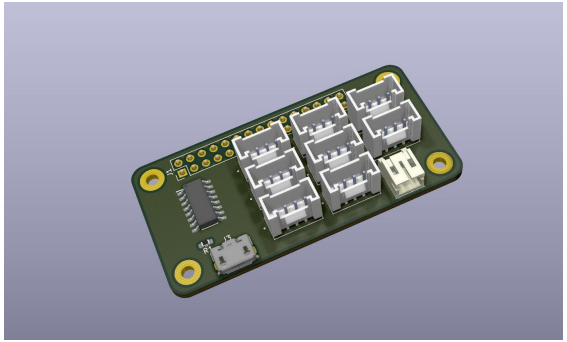


GPS Mezzanine CE



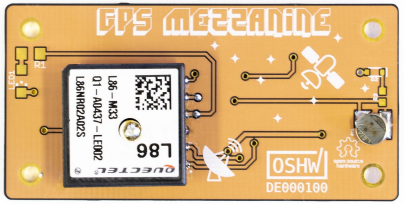
RoBo Mezzanine

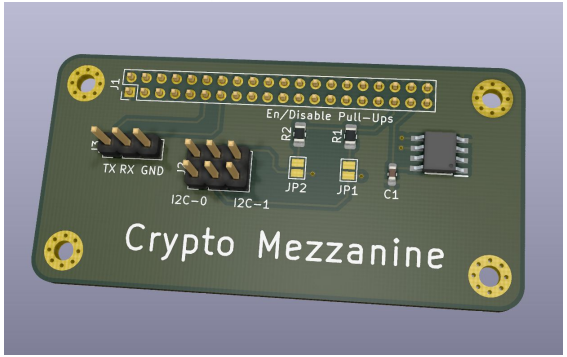
Community Member Mezzanines



Grove Mezzanine

GPS Mezzanine



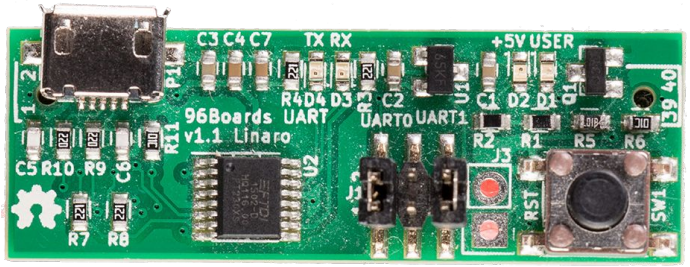


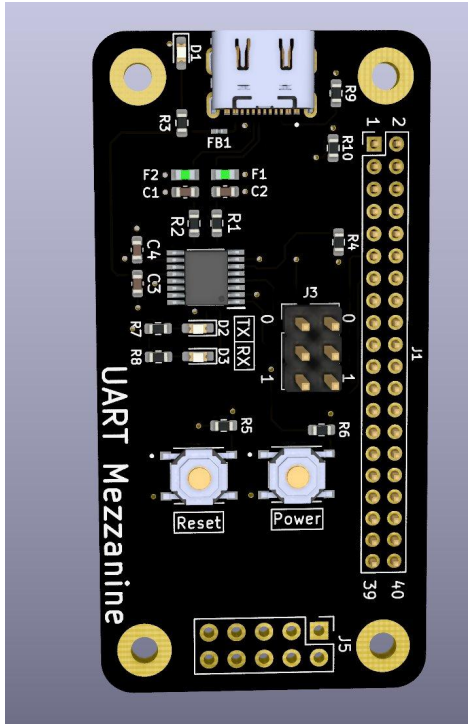
Crypto Mezzanine

Community Member Mezzanines

UART Mezzanine

- Drop in replacement
- CI Compatible
- USB C
- 2.54mm Breakout





Mezzanine Initiative

- Open Source Templates
- BSD License

CAD	Consumer Edition	IoT Edition
Kicad		
Eagle		
Altium		
gEDA		

https://github.com/96boards/mezzanine-community

How to contribute

- Bi-Weekly Zoom Call 16:00 UTC
- Github Repository https://github.com/96boards/mezzanine-community
- 96Boards Forum https://discuss.96boards.org/
- 96Boards Discord https://discord.com/invite/x5uMn5d

Github

Forum

Discord

96Boards CE V2

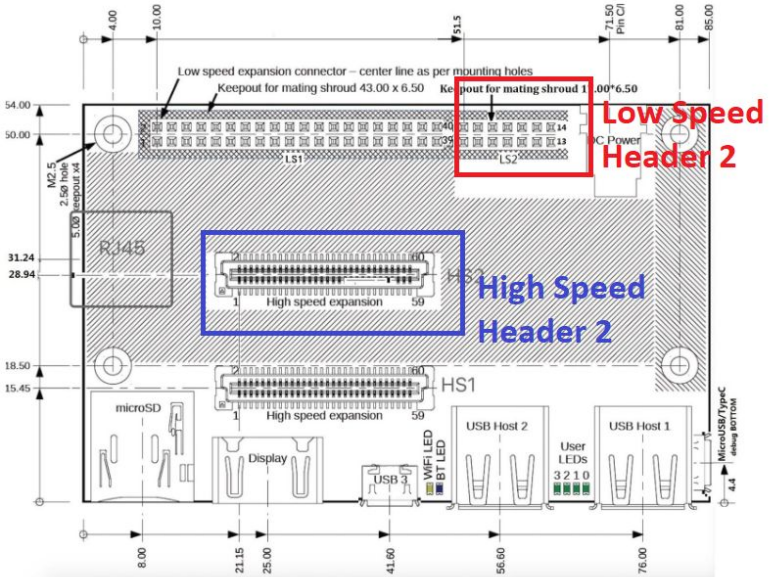
High Speed:

- 2x CSI 2-4 Lanes
- 2 Lane PCIE
- USB 3.0

(Signals Shared across HS1&HS2)

Low Speed:

- CAN
- Analog Audio



Thank you

Accelerating deployment in the Arm Ecosystem